F.Y.B.Sc. (With Credits)-Regular-Semester 2012 Sem I E-01-Electronics-I: Paper-I (Basic Electronics and Semiconductor Devices)

GUG/S/16/3307

Max. Marks: 50

Notes : 1. All questions are compulsory and carry equal marks.

- 2. Draw neat diagram wherever necessary.
- 3. Use of log table/calculator is permissible.

1. **Either:**

2.

3.

P. Pages: 2

Time : Three Hours

- Explain the construction and working of rheastate. a)
- b) Find equivalent capacitance across terminals A and B.



Explain the construction and working of autotransformer. 5 c) Explain the construction of ceramic capacitor. Give their advantages. 5 d) **Either:** What is rectifier ? Explain working of a full wave rectifier. Draw its input output waveform. 5 a) Explain the avalanche and Zener breakdown mechanism. 5 b) OR Discuss the working of PN junction in forward bias mode with its characteristics. c) 5 Explain diffusion and transition capacitance of a PN junction diode. d) 5 **Either:** What is BJT ? Explain the construction of PNP and NPN transistor with labelled diagram. 5 a) Differentiate between CE and CB configurations. 5 b)

OR

5

5

	c)	Explain the action of CB amplifier with suitable diagram.	5
	d)	Explain dc load line for CE mode in BJT. State the importance of load line.	5
4.		Either:	
	a)	Explain the construction and working of N-channel depletion MOSFET.	5
	b)	Define parameters of the FET and obtain the relation between them.	5
		OR	
	c)	Explain the working of TRIAC and draw its V-I characteristics.	5
	d)	Explain the working of SCR by drawing its two-transistor equivalent circuit.	5
5.	a)	Explain the concept of self and mutual inductance.	21/2
	b)	Differentiate between intrinsic and extrinsic semiconductor.	21/2
	c)	Define α (Alpha) and β (Beta) of a transistor and obtain the relation between them.	21/2
	d)	Differentiate between BJT and FET.	2 ¹ /2
