## B.Sc. (with Credits)-Regular-Semester 2012 Sem VI

## **B.Sc.4517 - Electronics : Paper-I (Compulsory) (Microprocessor, Interfacing and Microcontrollers)**

GUG/W/16/5634 P. Pages: 1 Time: Three Hours Max. Marks: 50 All questions are compulsory and carry equal marks. Notes: 1. 2. Draw a neat diagrams wherever necessary. 3. Use of log table / calculator is allowed. **Either** What is key debouncing? Explain hardware and software key debouncing. 10 1. a) OR Explain the interfacing of A/D converter ADC 0800 with microprocessor and draw 10 b) necessary diagram. **Either** What is delay subroutine? Explain the delay subroutine using register pair. 2. **10** a) OR Explain the concept of microprocessor based traffic control. b) 10 **Either** Draw a block diagram of 8086 and explain the function of each block in it. 3. a) 10 OR State any five addressing modes of 8086 microprocessor and explain each with example. 10 b) **Either** Draw internal architecture of 8051 microcontroller and explain. 10 4. a) OR List various groups of instruction set in 8051 microcontroller. 10 b) State the meaning of following instructions: MOV A, 020H ii) MOV RO, A iii) ADD A, @ RO i) Write a program in  $\mu$ c8051 for addition of two 8-bit numbers. 5. What is interfacing? Explain the needs of interfacing.  $2^{1/2}$ a) Explain temperature measurement using microprocessor.  $2^{1/2}$ b) Explain any two assembler directives in 8086 microprocessor.  $2^{1/2}$ c) State the features of 8096 microcontroller. d)  $2^{1/2}$